

Today's agenda

The semiconductor market:
Crisis? What crisis?
Luc Themelin, CEO

02

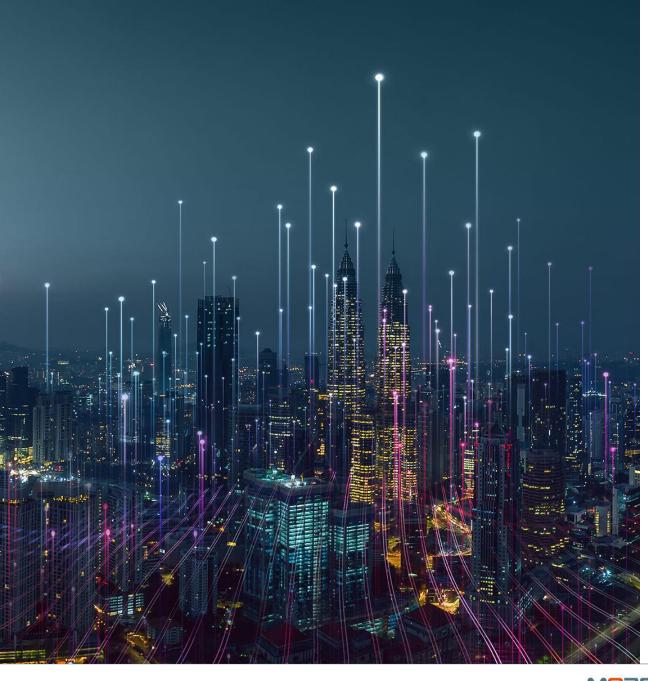
Mersen: A key link in the semiconductor value chain

Philippe Roussel, VP Global Strategic Marketing Electrical Power Alexandre Potier, CTO Advanced Materials

03

Strong market potential for Mersen

Luc Themelin, CEO



The semiconductor market: Crisis? What crisis?



Shortage dominates the news...

Technology Lead Times Begin to Slow, Chip Lead Times Begin Have Peaked Shortages Have Peaked Suggesting Shortages Bloomberg

Che New York Cimes

Automakers U.S. Sales



Sep 9, 2021, 11:00am EDT | 9 638 views

Semiconductor Shortage Is Far From Over, But These Stocks Stand To Gain Chip Shortage Makes Big Dent in



The global chip shortage is continuing to wreak havoc for the car giants

FINANCIAL TIMES

Asia chip shortage slams Apple and Nintendo output

James Kynge and Mercedes Ruehl NOVEMBER 3 2021

THE WALL STREET JOURNAL.

Car Companies Buckle Up for Extended Chip Shortage

Bottlenecks in Asia and the challenge of boosting output of the auto sector's more-basic computer chips could prolong the parts crisis into 2022

Updated Sept. 30, 2021 9:43 am ET



... but mainly reflects surging demand

Contextual



Lockdown measures led to an **explosion in demand for computers**

300 million

computers sold in 2020

+13% vs 2019

Source: International Data Corporation January 2021 (personal computing, laptops or desktops)

Structural

.

Ramp-up and rollout of **5G** networks

Increased demand for new smartphones

Increased complexity of 5G phones

5G smartphone shipments to account for more than 40% of global volume in 2021 and grow to 69% in 2025

Source: International Data Corporation January 2021



Devices **embedded** with sensors and software and interconnected over the internet

Number of IoT connected devices expected to rise from 7.74 billion in 2019 to 25.44 billion in 2030

Source: Statista



Safety features

Advanced driver
assistance systems

Despite declining sales (-30%), growing demand for **electronics**, **connectivity and safety features**

Expansion of the full-electric vehicle market

Estimated global market*: US\$527 billion in 2021 (vs US\$439 billion in 2020)







The way out of the crisis

Investments already planned to meet demand



SAMSUNG









TSMC to invest \$100 billion over the next three years

Will build and operate a **\$12 bn**plant in Arizona (USA) from 2021

to 2029



Samsung Electronics raised
its planned investment
in non-memory chips to \$151
billion (from \$115 billion) through
2030



Intel invests \$20 billion in new factories in Arizona (USA), will produce chips for other companies



China's SMIC will invest \$8.87 billion to build a chip plant in Shanghai, expanding capacity.



SK Hynix to invest 800 billion won in 'M16' in H2 2021, ahead of schedule.

Located in Icheon, Gyeonggi-do, M16 is the latest fab of SK Hynix



^{*} Sources: company press releases; Reuters and etnews

Tech sovereignty push brings opportunity

EU aims to double chip manufacturing to secure supply



Objective

Rebuild Europe's capacity to produce high-quality microelectronics

« European Chips Act »:

public-private investment of €40bn over 10 years September 15, 2021

France Relance 2030:

€6 bn to double capacity

October 12, 2021



Objective

Regain global leadership position on advanced chip manufacturing

« CHIPS for America Act »

\$52 billion in Federal investments

for domestic semiconductor research, design and manufacturing

June 8, 2021 (The U.S. Senate passed the U.S. Innovation and Competition Act)



Objective

China to produce 70% of the chips it uses by 2025

« Made in China 2025 »

China National IC Industry Investment Fund II (\$35B)

Provincial Funds (\$45B)

May 2015, National strategic plan and industrial policy of the Chinese Communist Party



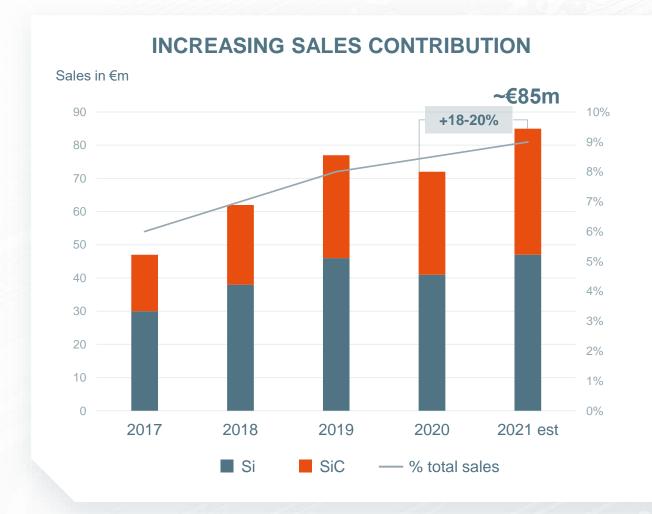


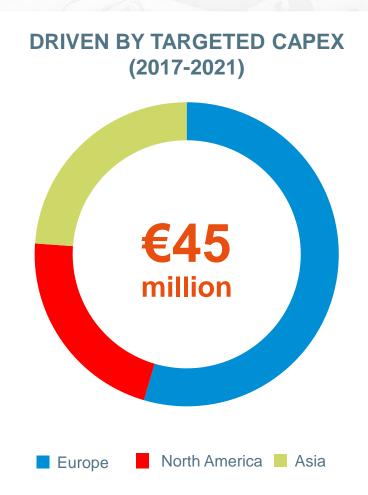
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Mersen: A key link in the semiconductor value chain

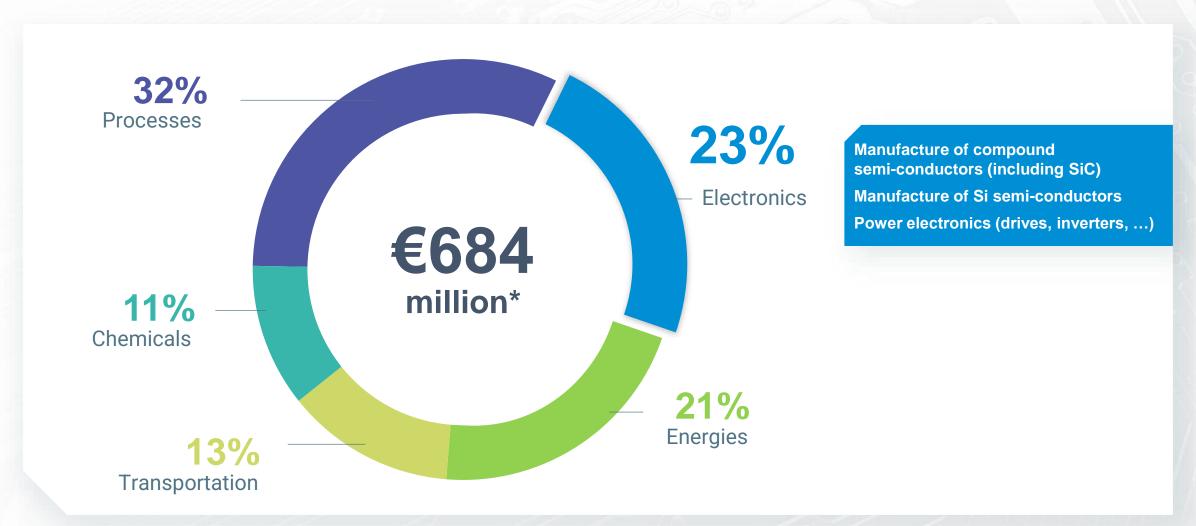


Semiconductors: A growing share of Mersen's business



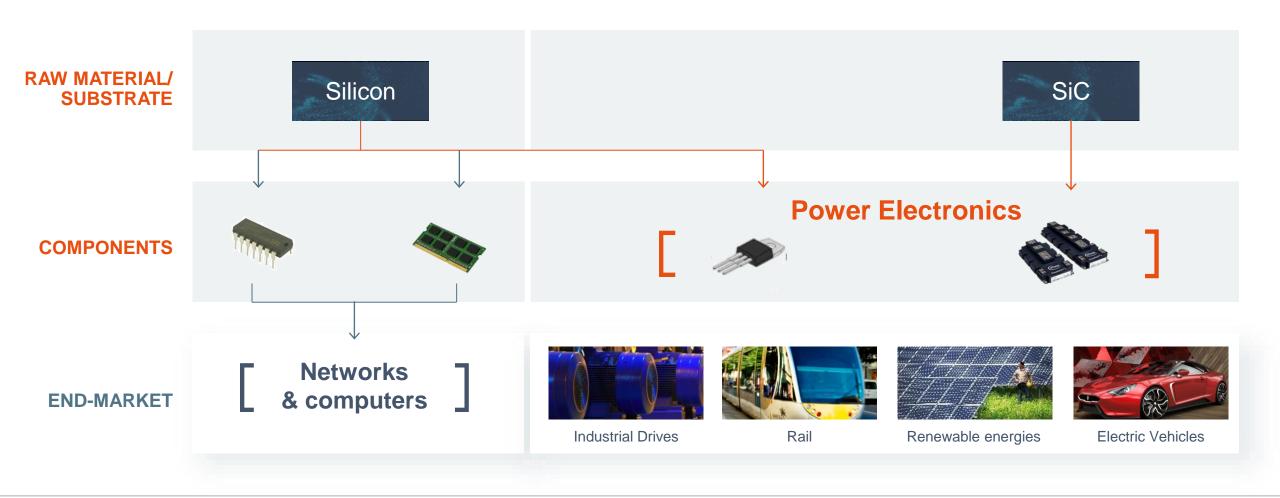


Semiconductors are part of the electronics end-market



* 9 months sales

Power Electronics offers high growth potential across Mersen's markets



From sand to Si-semiconductor: an elaborate industrial process



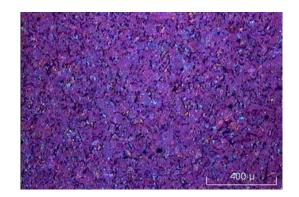
SiC semiconductor: a sophisticated new product

15 different steps Ion Photo Deposition SiC epitaxy **Etching** Annealing **Implantation** lithography Crystal **Applied Materials** Aixtron **Applied Materials** LAM Research ASML PVA Tepla Nuflare Axcelis LAM Research Centrotherm Canon Tokyo Electron Tokyo Electron (TEL) Nissin Ion **GTAT ASM International** Nikon **Applied Materials** LPE Tokyo Electron Sumitomo **NORTH NORTH ASIA EUROPE EUROPE ASIA AMERICA AMERICA** COMPONENT MANUFACTURERS: AFTER-MARKET Wolfspeed. **(infineon** SICC. ROHM **SiCrystal** Wolfspeed. SK siltron MICROCHIP 57 TANKEBLUE **BOSCH** SHOWA GT ADVANCED

OEMS

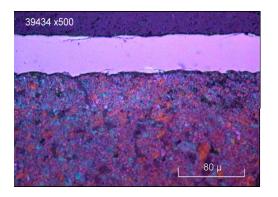






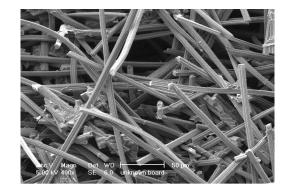
FINE ISOSTATIC GRAPHITE

- High temperature resistance (3,000°C)
- High purity level (< 5 ppm)
- Mechanical stability
- Electrical & Thermal conductivity
- Chemical stability
- Machinability
- Lightweight



SILICON CARBIDE CVD COATING

- Purity
- Mechanical strength
- Thermal conduction
- High temperature resistance (2,000°C)
- Chemical stability, corrosion resistance



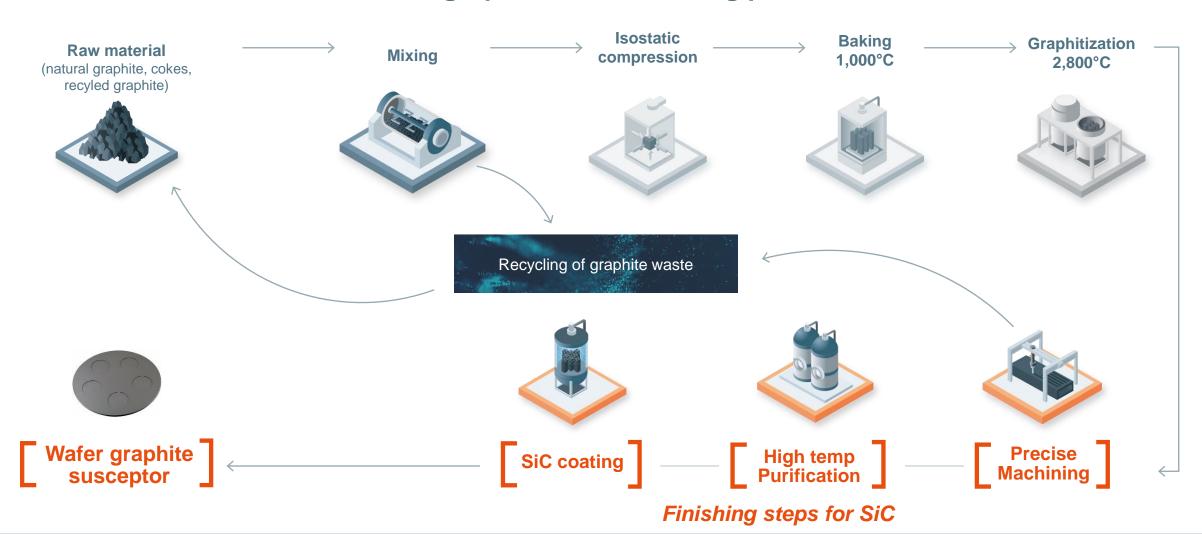
CARBON INSULATION

- High temperature resistance (2,800°C)
- High purity
- Thermal insulation
- Thermal uniformity
- Chemical stability
- Machinability

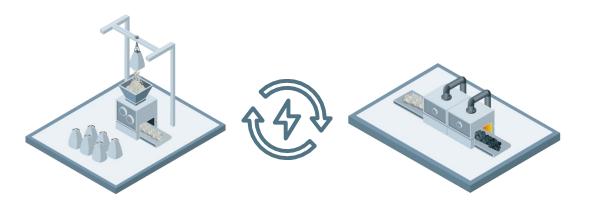


A process that emits zero CO₂

Mersen graphite manufacturing process



Energy efficiency in Mersen's carbon insulation process



Savings
300 MWh/year
vs Batch process

New process (Continuous)

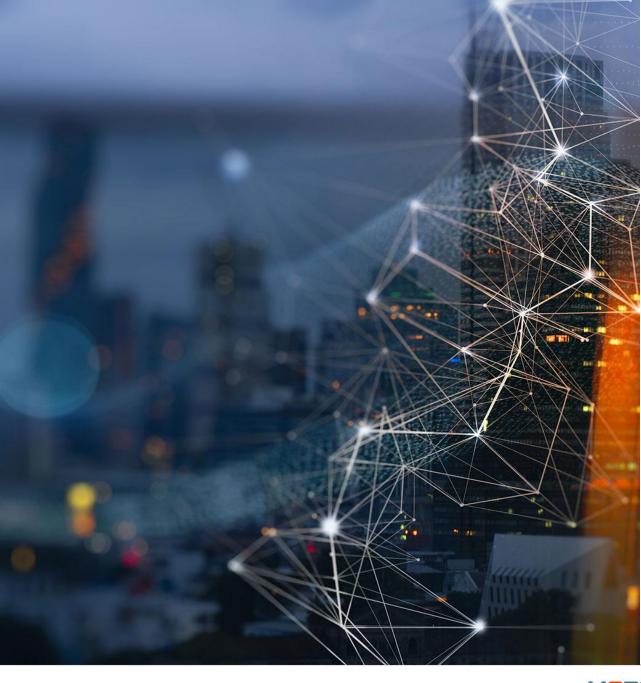
Energy efficiency at customer sites

At 2,400°C,
(SiC sublimation temperature)
Mersen carbon insulation - very high insulating power - allows savings of 15% of energy vs standard product

Savings of 25GWh/year

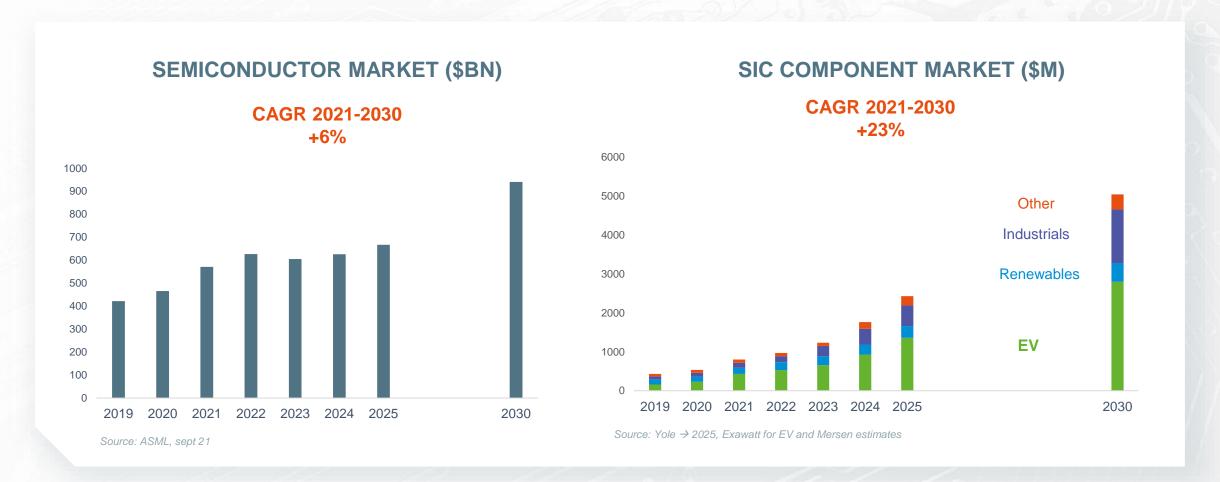
Equivalent to the CO₂ emissions of 3,000 cars per year





Strong market potential for Mersen

The semiconductor market will see significant growth through 2030



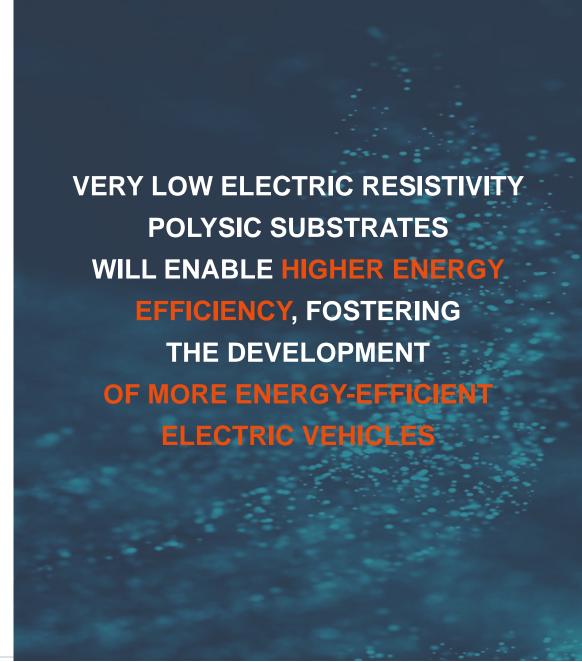
Strategic partnership with Soitec





Strategic partnership to develop a new range of PolySiC substrates for the EV market

Soitec and Mersen teams leverage CEA-Leti
Substrate innovation center to validate progress
towards industrialization





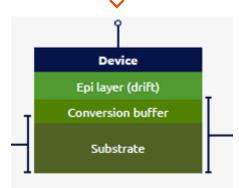
Traditional SiC vs SmartSiCTM

Mersen in the process

TRADITIONAL PVT PROCESS (SIC monocrystalline)







Mersen now in the device

SOITEC SMARTSIC™ ON POLY-SIC PRODUCT



COST



High



Low (CVD + SmartCut)

DOPING



Limited by technology



High to reduce resistivity

Mersen's strengths

Longstanding ties with customers

Customer intimacy

OEM and replacement market

Significant market share in Si, much more in SiC





Mersen's strategy

for the semiconductor market



Growth in line with Si market, thanks to our strong positions in particular in ion implant and ALD (Atomic Layer Deposition)



Consolidate our positioning on the SiC semiconductor market in the sublimation process with our historical partners



Strengthen cooperation with Soitec on SmartSIC technology



Electrical expertise Semiconductors need to be protected: components for power conversion



Mersen's medium-term potential in semiconductors



~25-30% EBITDA margin

Capex €8m-12m/ year (excl. SmartSIC)

SmartSIC Capex under review



Key takeaways



Chip "crisis" offers major growth opportunity for Mersen



Silicon-dominated semiconductor market showing robust growth



SiC applications including EVs promise fast expansion

Mersen positioned for profitable growth in line with sustainability objectives

